

Specification

G420XW02 V0

Version January 2008

Note: This specification is subject to change without prior notice

Product Specifications

42.0" WXGA Color TFT-LCD Module

Model Name: G420XW02 V.0

(*) Preliminary Specifications

() Final Specifications

Note: This Specification is subject to change without notice.

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1. General Description

This specification applies to the 42.0 inch Color TFT-LCD Module G420XW02 V0. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 42.0 inch. This module supports 1366x768 XGA-Wide modes (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The G420XW02 V0 has been designed to apply the 8-bit 1 channel LVDS interface operation. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

The data on this specification sheet is applicable when LCD module is placed in landscape position.

* General Information

Items	Specification	Unit	Note
Display Mode	AMVA		
Active Screen Size	42.02	Inch	
Display Area	930.25 (H) x 523.01(V)	mm	
Outline Dimension	957.45(H) x 550.20(V) x 60.00(D)	mm	With inverter
Driver Element	a-Si TFT active matrix		
Display Colors	16.7M	Color	
Number of Pixels	1366x768	Pixel	
Pixel Pitch	0.681	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
RoHS	Compliance		
Surface Treatment	AG, 3H		

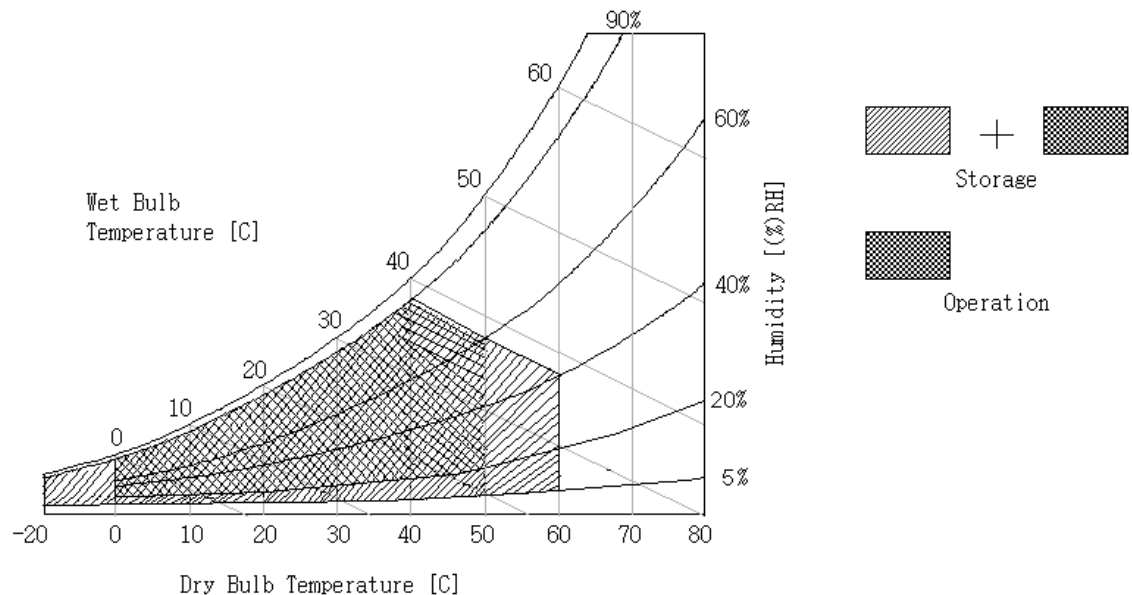
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Parameter	Symbol	Min.	Max.	Unit	Note
Logic/LCD Driving Voltage	V_{dd}	-0.3	14	V	1
Input Voltage of Signal	V_{in}	-0.3	3.6	V	1
BLU Input Voltage	V_{DDB}	-0.3	26	V	1
BLU Control Voltage	V_{BLON}	-0.3	5.5	V	1
Operating Temperature	T_{OP}	0	50	°C	2
Storage Temperature	T_{ST}	-20	60	°C	2
Operating Ambient Humidity	H_{OP}	10	90	%RH	2
Storage Humidity	H_{ST}	10	90	%RH	2

Note 1: Duration = 50msec

Note 2: Maximum Wet-Bulb should be 39°C and No condensation.



3. Electrical Specification

The G420XW02 V0 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter.

3-1 Electrical Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
LCD							
Power Supply Input Voltage		V_{dd}	11.4	12.0	12.6	V	
Power Supply Input Current		I_{dd}	-	0.75	(0.875)	A.	1
Power Consumption		P_C	-	9	(10)	W	1
Inrush Current		I_{RUSH}	-	-	(5)	A	5
LVDS Interface	Differential Input High Threshold Voltage	V_{TH}			+100	mV	4
	Differential Input Low Threshold Voltage	V_{TL}	-100			mV	4
	Common Input Voltage	V_{CIM}	1.10	1.25	1.40	V	
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.4		3.3	V	
	Input Low Threshold Voltage	V_{IH} (Low)	0		0.7	V	
Backlight Power Consumption			-	180	-	W	2
Lamp Life Time			50000	60,000		hr	3

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Note :

1. $V_{dd}=12.0V$, $f_v=60Hz$, $f_{CLK}=76\text{ Mhz}$, $25^\circ C$, V_{dd} Duration time= $500\ \mu s$, Test pattern : white pattern
2. The Backlight power consumption shown above does include loss of external inverter at $25^\circ C$.
The used lamp current is the lamp typical current
3. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25\pm 2^\circ C$.
4. $V_{CIM} = 1.25V$

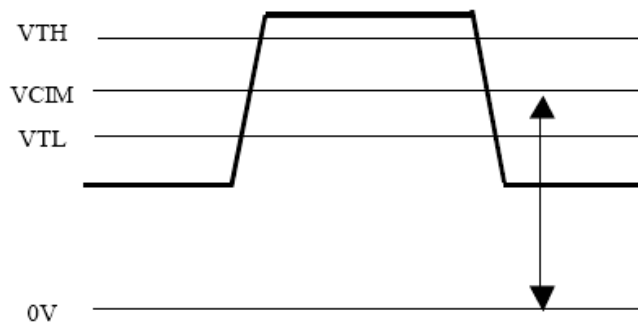
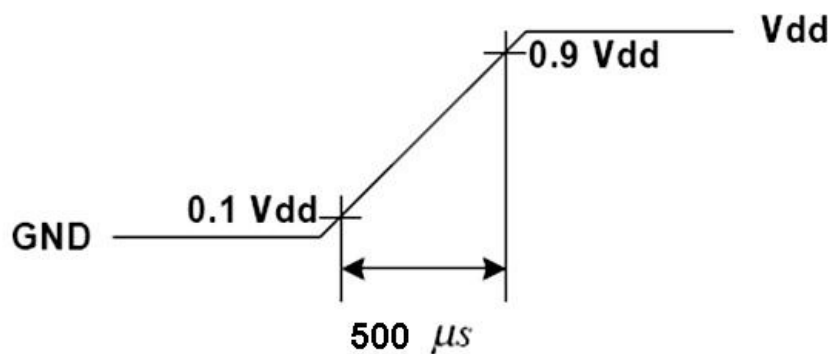


Figure : LVDS Differential Voltage

5. Measurement Condition: Rising time = $500\ \mu s$



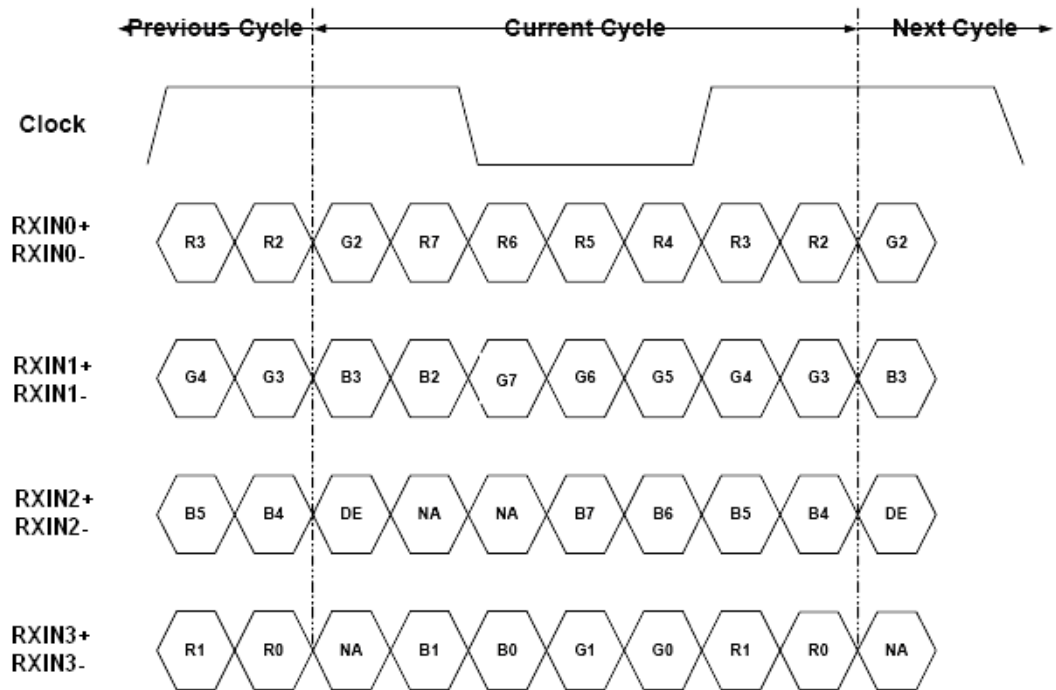
3-2 Interface Connections

LCD connector: FI-X30SSL-HF (JAE) or equivalent

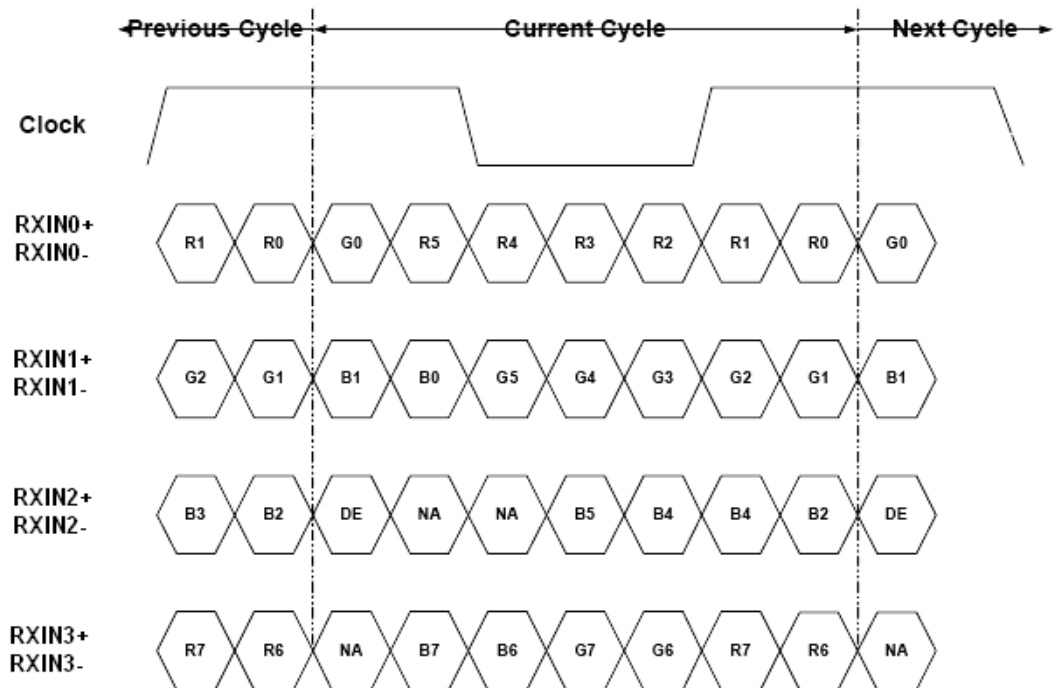
Mating connector: FI-30C2L (JAE) or equivalent

Pin No	Symbol	Function	Note
1	V _{dd}	+12V, DC, Regulated	
2	V _{dd}	+12V, DC, Regulated	
3	V _{dd}	+12V, DC, Regulated	
4	V _{dd}	+12V, DC, Regulated	
5	GND	Ground and Signal Return	
6	GND	Ground and Signal Return	
7	GND	Ground and Signal Return	
8	GND	Ground and Signal Return	
9	LVDS Option	Low/Open for Normal (NS), High for JEIDA	Default : NS mode
10	Reserved	Open or High	For AUO internal test
11	GND	Ground and Signal Return for LVDS	
12	RXIN0-	LVDS Channel 0 negative	
13	RXIN0+	LVDS Channel 0 positive	
14	GND	Ground and Signal Return for LVDS	
15	RXIN1-	LVDS Channel 1 negative	
16	RXIN1+	LVDS Channel 1 positive	
17	GND	Ground and Signal Return for LVDS	
18	RXIN2-	LVDS Channel 2 negative	
19	RXIN2+	LVDS Channel 2 positive	
20	GND	Ground and Signal Return for LVDS	
21	RXCLKIN-	LVDS Clock negative	
22	RXCLKIN+	LVDS Clock positive	
23	GND	Ground and Signal Return for LVDS	
24	RXIN3-	LVDS Channel 3 negative	
25	RXIN3+	LVDS Channel 3 positive	
26	GND	Ground and Signal Return for LVDS	
27	Reserved	Open or High	For AUO internal test
28	Reserved	Open or High	For AUO internal test
29	GND	Ground and Signal Return	
30	GND	Ground and Signal Return	

LVDS Option = High → JEIDA



LVDS Option = Low/Open → NS



Backlight Connector Pin Configuration

1. Electrical specification

No	Item	Symbol	Test Condition	Min	Typ	Max	Unit	Note	
1	Input Voltage	V_{DDB}	--	22.8	24	25.2	V		
2	Input Current	I_{DDB}	$V_{DDB}=24V$ 100%brightness	7.13	7.5	7.86	A	1	
3	Input Power	P_{DDB}	$V_{DDB}=24V$ 100%brightness	171	180	189	W	1	
4	Input Inrush current	I_{RUSH}	$V_{DDB}=24V$ 100%brightness			TBD	A	2	
5	Output Frequency	F_{BL}	$V_{DDB}=24V$		36		KHz		
6	On/Off Control Voltage	V_{BLON}	On	$V_{DDB}=24V$	2.0	3.3	5	V	
			Off	$V_{DDB}=24V$	0.0	-	0.8	V	
7	On/Off Control Current	I_{BLON}	$V_{DDB}=24V$	0		2	mA		
8	External PWM Control Voltage	EV_{PWM}	Max	--	2.0		3.3	V	
			Min	--	0		0.8	V	
9	External PWM Control Current	EI_{PWM}	Max	PWM = 100%	0		2	mA	
			Min	PWM = 30%	0		2	mA	
10	External PWM Duty Ratio	ED_{PWM}	--	30		100	%		
11	External PWM Frequency	EF_{PWM}	--	150	180	300	Hz		
12	Internal PWM Control Voltage	IV_{PWM}	$V_{DDB}=24V$	0		3.3	V		

($T_a=25\pm 5^{\circ}C$, Turn on for 45 mins)

Note 1: P_{DIM} =Open/3.3V

Note 2: Duration = 20ms

2. Input specification

Connector 1: S14B-PH-SM4-TB (JST) or equivalent

CN1 : S14B-PH-SM4-TB(JST)		
No	Signal Name	Feature
1	V _{DDB}	Operating Voltage Supply, +24V DC regulated
2	V _{DDB}	Operating Voltage Supply, +24V DC regulated
3	V _{DDB}	Operating Voltage Supply, +24V DC regulated
4	V _{DDB}	Operating Voltage Supply, +24V DC regulated
5	V _{DDB}	Operating Voltage Supply, +24V DC regulated
6	GND	Ground and Current Return
7	GND	Ground and Current Return
8	GND	Ground and Current Return
9	GND	Ground and Current Return
10	GND	Ground and Current Return
11	DET	Detect function output; Normal display: ($\leq 0.8V$), Fail: open collector.
12	V _{BLON}	GND: BL Off; Open/High (3.3V): BL On
13	PDIM	External PWM (AC Signal Control Duty); Internal PWM (DC Power Control Duty, 0~3.3V); Open/High (+3.3V, 100% Duty) for 100%
14	PDIM Selection	GND: External PWM dimming; Open/High(3.3V): Internal PWM dimming

Connector 2: S12B-PH-SM4-TB (JST) or equivalent

CN2 : S12B-PH-SM4-TB(JST)		
No	Signal Name	Feature
1	V _{DDB}	Operating Voltage Supply, +24V DC regulated
2	V _{DDB}	Operating Voltage Supply, +24V DC regulated
3	V _{DDB}	Operating Voltage Supply, +24V DC regulated
4	V _{DDB}	Operating Voltage Supply, +24V DC regulated
5	V _{DDB}	Operating Voltage Supply, +24V DC regulated
6	BLGND	Ground and Current Return
7	BLGND	Ground and Current Return
8	BLGND	Ground and Current Return
9	BLGND	Ground and Current Return
10	BLGND	Ground and Current Return
11	NC	No Connection
12	NC	No Connection

3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

Vertical Frequency Range A (60Hz)

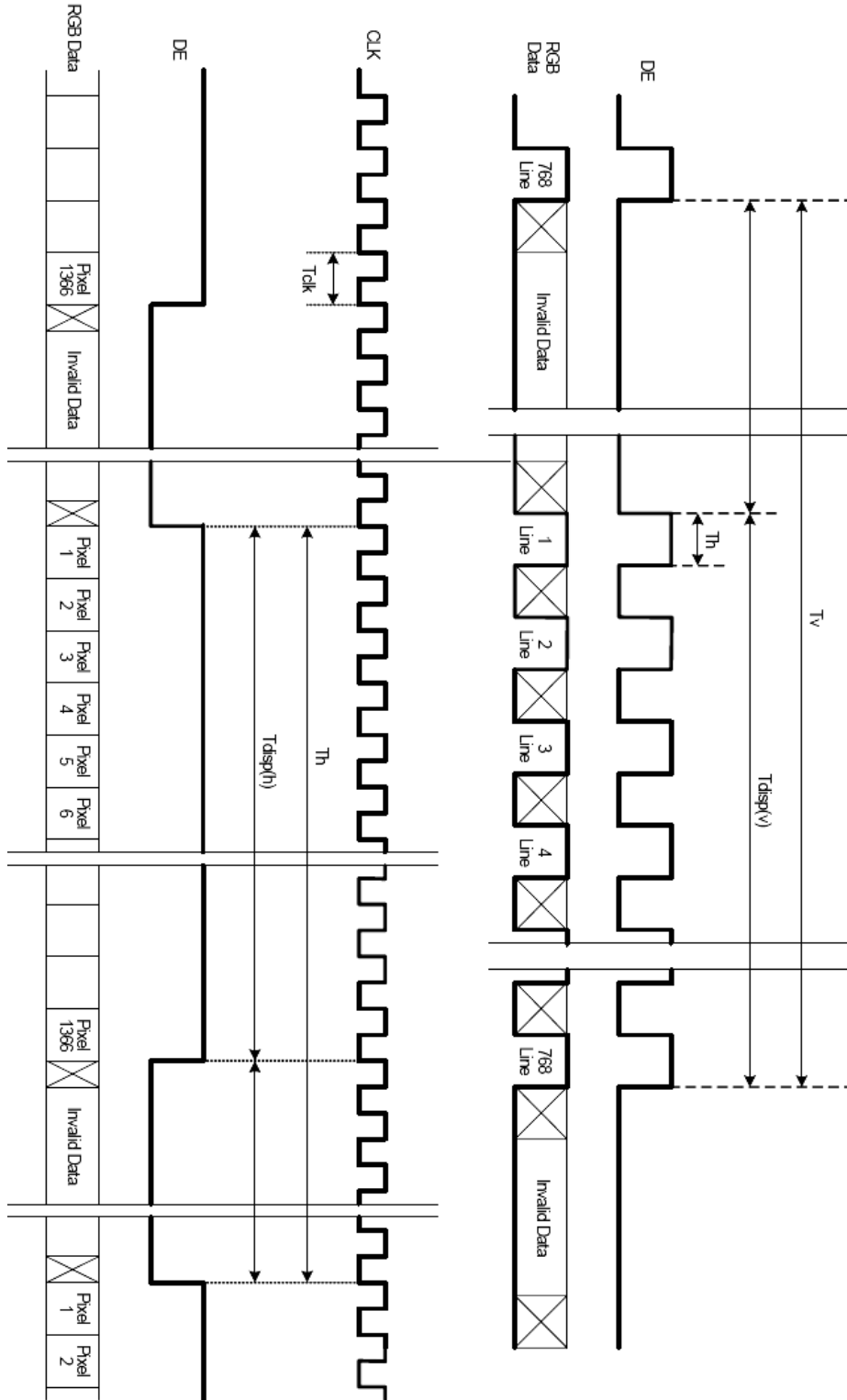
Signal	Item	Symbol	MIN	TYP	MAX	Unit
Vertical Section	Period	Tv	789	806	822	Th
	Active	Tdisp(v)	768			Th
	Blanking	Tblk(v)	21	38	54	Th
Horizontal Section	Period	Th	1414	1560	1722	Tclk
	Active	Tdisp(h)	1366			Tclk
	Blanking	Tblk(h)	48	194	356	Tclk
Vertical Frequency	Frequency	Freq	58	60	62	Hz
Horizontal Frequency	Frequency	Freq	47.32	48.72	49.32	KHz
Clock	Frequency	FCLK	65	76	88	MHz

Vertical Frequency Range B (50Hz)

Signal	Item	Symbol	MIN	TYP	MAX	Unit
Vertical Section	Period	Tv	789	806	822	Th
	Active	Tdisp(v)	768			Th
	Blanking	Tblk(v)	21	38	54	Th
Horizontal Section	Period	Th	1414	1560	1722	Tclk
	Active	Tdisp(h)	1366			Tclk
	Blanking	Tblk(h)	48	194	356	Tclk
Vertical Frequency	Frequency	Freq	48	50	52	Hz
Horizontal Frequency	Frequency	Freq	39.45	40.38	41.10	KHz
Clock	Frequency	Freq	54	63	74	MHz

Note: Typical value refer to VESA STANDARD

3-4 Signal Timing Waveforms



3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Color		COLOR DATA REFERENCE																							
		RED								GREEN								BLUE							
		MSB				LSB				MSB				LSB				MSB				LSB			
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
RED	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		

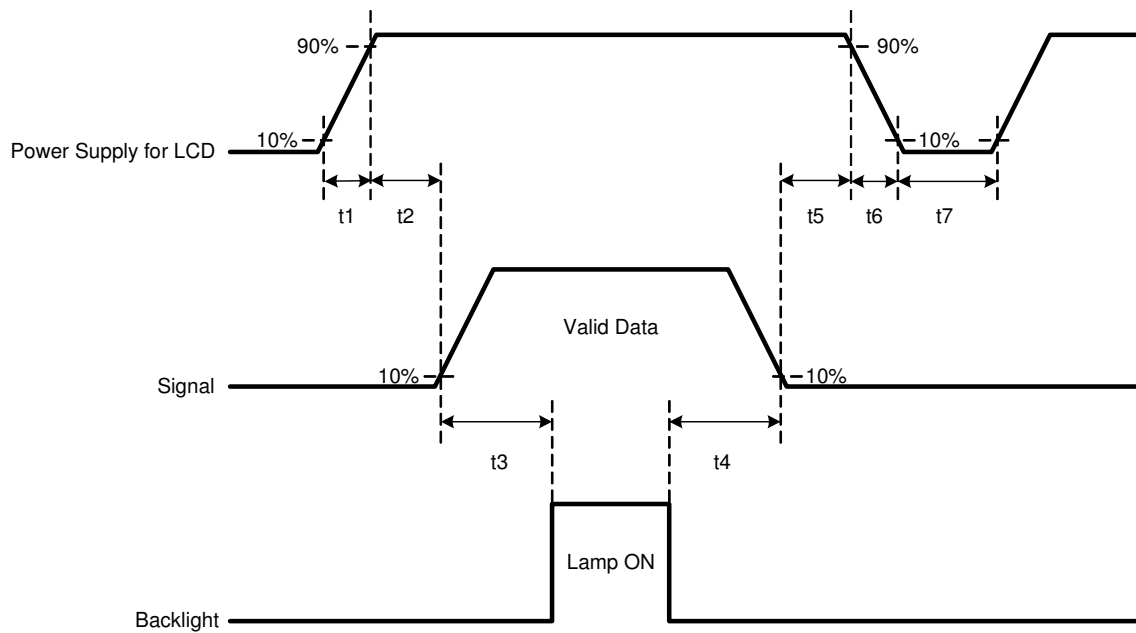
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
GREEN	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0		

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0		
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0		
BLUE	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

3-6 Power Sequence

1. Power sequence of panel



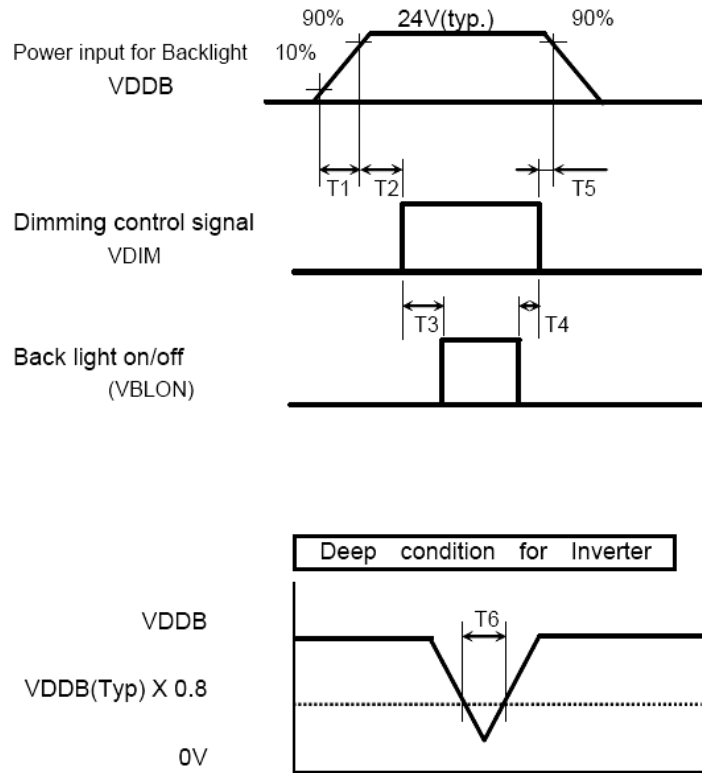
Parameter	Values			Units
	Min.	Typ.	Max.	
t1	0.5	-	20	ms
t2	20	-	50	ms
t3	800	-	-	ms
t4	200	-	-	ms
t5	50	-	-	ms
t6	0.5	-	30	ms
t7	1.0	-	-	s

*: If t3=200ms, input black signal till 700ms from system is necessary. In case of t3<200ms, the abnormal display will be happened. But it will not damage timing controller.

Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution: The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

2 Power Sequence of Inverter



Parameter	Values			Units
	Min.	Typ.	Max.	
T1	20	-	-	ms
T2	0	-	-	ms
T3	500	-	-	ms
T4	0	-	-	ms
T5	1	-	-	ms
T6	-	-	10	ms

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

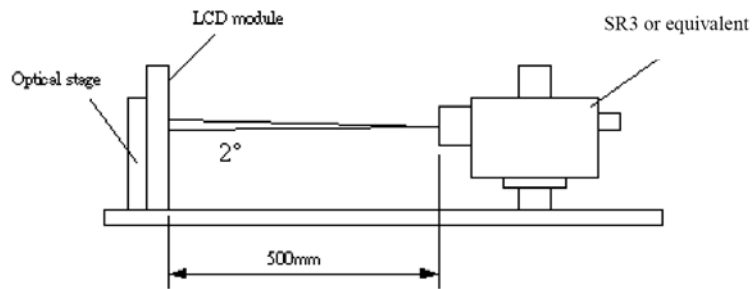


Fig.4-1 Optical measurement equipment and method

Parameter	Symbol	Values			Units	Notes	
		Min.	Typ.	Max.			
Contrast Ratio	CR	1000	1500	-		1	
Surface Luminance, white	LWH	400	500	-	cd/m ²	2	
Luminance Variation	δ_{WHITE} 5 p	-	-	1.3		3	
Response Time (Average)	T _Y		8	25	ms	4,5 (Gray to Gray)	
	Rise Time	T _r	-	15	ms	4	
	Decay Time	T _f	-	5	ms	4	
Color Chromaticity	RED	R _X	Typ -0.03	0.640	Typ +0.03		
		R _Y		0.330			
	GREEN	G _X		0.290			
		G _Y		0.600			
	BLUE	B _X		0.150			
		B _Y		0.060			
	WHITE	W _X		0.280			
		W _Y		0.290			
Viewing Angle						CR>10	
	x axis, right($\varphi=0^\circ$)	θ_r	-	89	-	Degree	6
	x axis, left($\varphi=180^\circ$)	θ_l	-	89	-		
	y axis, up($\varphi=90^\circ$)	θ_u	-	89	-		
	y axis, down ($\varphi=0^\circ$)	θ_d	-	89	-		

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio(CR)} = \frac{\text{Surface Luminance with all "white" pixels}}{\text{Surface Luminance with all "black" pixels}}$$

2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When $V_{DDB} = 24V$, $I_{DDB} = 7.5 A$. $L_{WH} = L_{on1}$, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

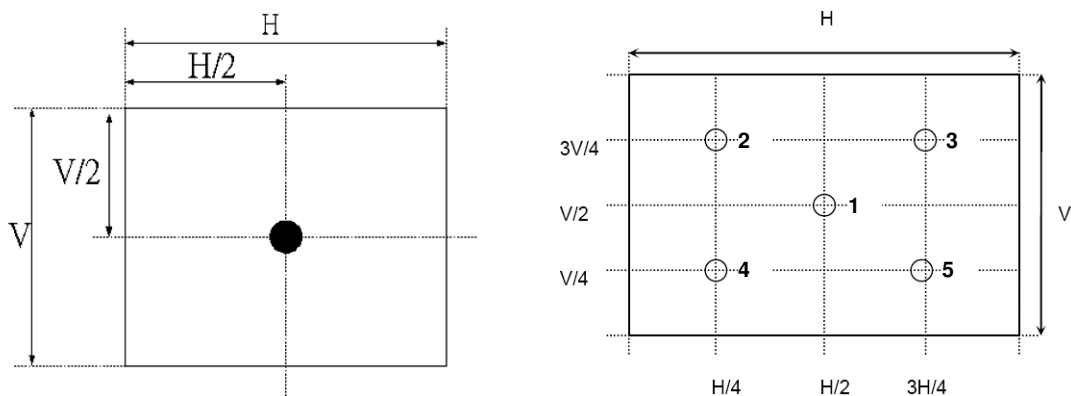


Fig.4-2 Optical measurement point

3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as:

$$\delta_{WHITE(5P)} = \frac{\text{Maximum}(L_{on1}, L_{on2}, \dots, L_{on5})}{\text{Minimum}(L_{on1}, L_{on2}, \dots, L_{on5})}$$

4. Response time is the time required for the display to transition from white(L_{255}) to black(L_0) (Decay Time, $Tr_D = Tf$) and from black(L_0) to white(L_{255}) (Rise Time, $Tr_R = Tr$). For additional information see Fig. 4-3.

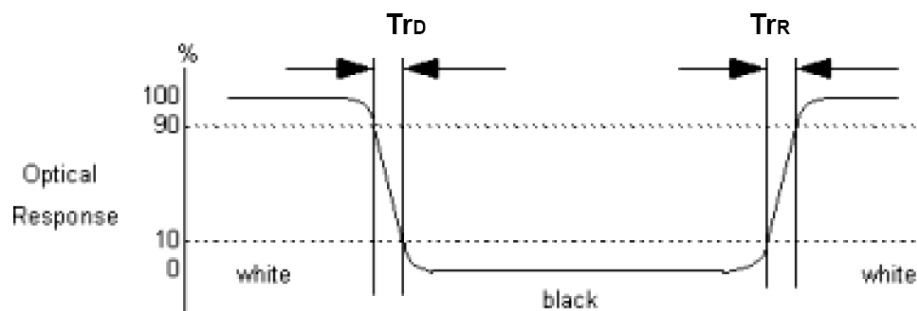


Fig.4-3 Response time

5. The response time is defined as the following figure and shall be measured by switching the input signal among 0%, 25%, 50%, 75%, 100% luminance. For additional information see Fig. 4-4.

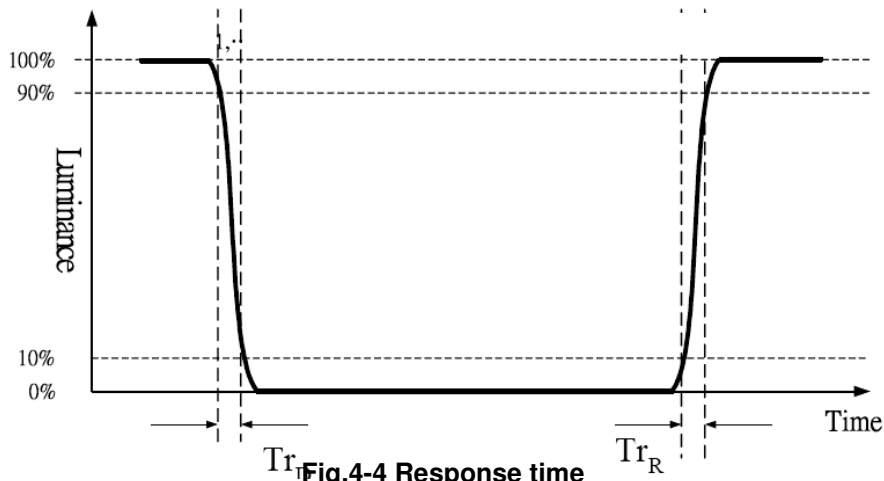


Fig.4-4 Response time

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-5. (Optical measurement by SR3)

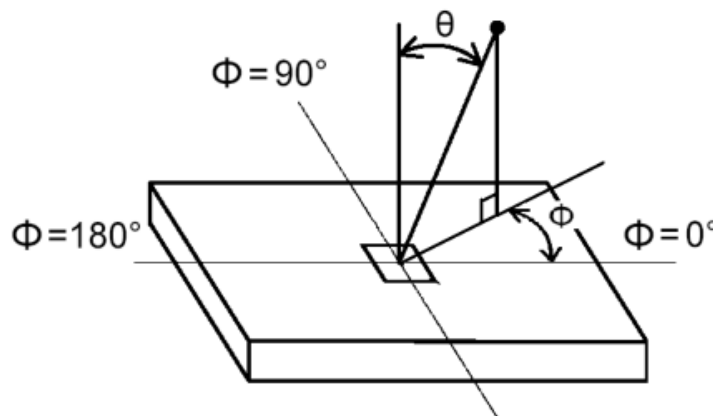


Fig.4-5 Viewing Angle Definition

5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model G420XW02 V0. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal (typ.)	957.45 mm
	Vertical (typ.)	550.20 mm
	Depth (max.)	60.00 mm(with inverter)
Bezel Opening Area	Horizontal (typ.)	938.95 mm
	Vertical (typ.)	531.01 mm
Display Active Area	Horizontal (typ.)	930.25 mm
	Vertical (typ.)	523.01 mm
Weight	13000 g (Max.)	
Surface Treatment	AG, 3H	

6. Reliability

No	Test Item	Condition
1	High temperature storage test	Ta=60°C , 300hr judge
2	Low temperature storage test	Ta=-20°C , 300hr judge
3	High temperature/High humidity operation test	Ta=50°C , 80%RH, 300hr judge
4	High temperature operation test	Ta=50°C , 300hr judge
5	Low temperature operation test	Ta=-5°C , 300hr judge
6	Thermal shock	-20°C/0.5hr ~ 60°C/0.5hr, 100cycle
7	Vibration test (non-operating)	Wave form: Random Vibration level: 1.5G RMS, Bandwidth: 10-500Hz Duration: X, Y, Z (30min one time each direction)
8	Shock test (non-operating)	Shock level: 40G Waveform: half sine wave, 11ms Direction: ±X, ±Y, ±Z (One time each direction)
9	Vibration test (with carton)	Wave form: Random Vibration level: 2.19G RMS, Bandwidth: 1~500Hz Duration: X, Y, Z (30min each direction)
10	Drop test (with carton)	Height: 30.5cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)

7. International Standard

7-1. Safety

- (1) UL60065, Underwriters Laboratories, Inc. (AUO file number : E204356)
Audio, video and similar electronic apparatus, safety requirement .
- (2) CSA E60065, Canadian Standards Association
Audio, video and similar electronic apparatus, safety requirement .
- (3) IEC 60065 ver. 7th, European Committee for Electro technical Standardization (CENELEC)
Audio, video and similar electronic apparatus, safety requirement .

7-2. EMC

- (1) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute (ANSI), 1992
- (2) CISPR 20 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment. ” International Special committee on Radio Interference.
- (3) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment. ” European Committee for Electrotechnical Standardization. (CENELEC), 1998

